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[0001] The present invention relates to the field of implantable medical devices and, in particular, to a cardiac stimulation device employing a CF_x battery for improved performance and a system for monitoring charge level of the battery.

[0002] Implantable medical devices are typically battery powered devices that are implanted within the patient's body to have therapy available to the patient on a continuous basis. Battery failure is a particular problem with these devices as replacement of batteries often requires invasive surgical procedures. One particularly common type of implantable medical device is an implantable cardiac stimulation device.

[0003] Implantable cardiac stimulation devices, such as pacemakers and implantable cardioverter/defibrillators (ICD's), are employed to monitor cardiac activity and to provide therapy for patients with a variety of heart arrhythmias. Typically, these devices include sensors, that sense heart function and physiological parameters, and waveform generation and delivery systems, that provide electrical waveforms to the heart to correct arrhythmias and to ensure that more proper function of the heart is maintained. As the devices are implanted in a patient, it is desirable that the devices be as small and lightweight as possible in order to minimize impact on the patient.

[0004] Implantable cardiac stimulation devices are typically provided with batteries to power the monitoring and therapy delivery circuits. Due to the size constraints, the batteries used in implantable cardiac stimulation devices must be very small in size and yet able to

provide power over a long period of time. Once the device is implanted, replacement of batteries typically involves invasive surgery. Hence, there is a strong desire to have small batteries that can provide significant power output to power the implantable device for extended periods of time. Known pacemaker devices typically use lithium iodine (LiI), commonly referred to as lithium batteries. Lithium batteries offer relatively high energy storage density and have known, predictable discharge characteristics.

[0005] While lithium batteries are commonly used for implantable cardiac stimulation devices, these batteries require additional circuitry that degrade device performance. Specifically, the performance characteristics of these batteries often require that additional circuitry be added to the device, thereby resulting in consumption of limited space in the implantable device and also consumption of limited power, or this additional circuitry has performance characteristics that limit the useful life of the implantable device.

[0006] For example, **FIG. 1** illustrates a high-level conventional pacemaker circuit diagram of the prior art. Lithium batteries are typically not capable of providing pacing pulses at increased energy levels. As is shown in **FIG. 1**, a typical lithium battery and a decoupling capacitor are often connected in parallel to address this problem. The decoupling capacitor is used to accumulate electrical charge between pacing pulse events to enable the pacemaker to periodically deliver a pulse of energy at a greater rate than a lithium battery is capable of providing directly. The decoupling capacitor is continuously charged by the lithium battery and discharged upon a pacing event.

[0007] However, known implementations of the decoupling capacitors of the requisite electrical properties occupy a large fraction of the overall volume of the pacemaker device. As pacemakers shrink in size due to product refinement, the size of the capacitor is becoming an increasingly larger proportion of the total pacemaker volume and is

presenting a limitation to further reduction in the size and weight of pacemaker devices.

[0008] FIG. 1 illustrates another aspect of known pacemaker designs, in particular, a voltage tripler that is part of the control circuitry for the pacemaker. The control circuitry performs the basic timing and monitoring functions of the device and delivers the pacing pulses to the patient's heart. The voltage tripler increases the voltage delivered by the lithium battery in order to provide a sufficient potential for effectively stimulating the heart. A lithium battery will have an open circuit voltage of approximately 2.7 VDC in a fully charged condition and approximately 2 VDC near the end of its life and thus requires a voltage tripler to generate the more than 5 VDC required for an effective pacing pulse. However, the voltage tripler is a source of overall system inefficiency as each voltage multiplication incurs some degree of loss.

[0009] A further drawback to the lithium battery will be apparent considering the output voltage characteristics illustrated in FIG. 2, which shows a typical voltage vs. charge delivery graph for typical lithium batteries. Multi-chamber pacing is a feature of many pacemaker systems and comprises supplying pacing stimuli to two different sites in the heart as opposed to pacing a single site. Typical parameters for a single-chamber pacing system with a lithium iodide battery at beginning of life would be an open circuit voltage of approximately 2.7 V (8.1 V after the voltage tripler) with an internal battery impedance of 300 Ω and a single lead of 500 Ω impedance. The voltage across the lead is regulated to be approximately 5 V and the pulsed current would be approximately 10 mA. Pulses are approximately 1 ms in duration and are applied every second, thus drawing a time averaged current of approximately 10 μ A.

[0010] Similar multi-chamber pacing to two sites through two 500 Ω leads connected in parallel would draw a current pulse of approximately 20 mA and a time average current of 20 μ A. However, as a lithium battery is discharged, the open circuit voltage drops while the

internal resistance increases. After delivering approximately 900 mA-h, a typical lithium battery's output voltage decreases to approximately 2.4V and the internal impedance increases to approximately 10 k Ω . Under these parameters, delivering to two leads with 20 μ A average current
5 pulls the battery output voltage down to 2.2 V ($2.4 \text{ V} - 20\text{E-}6 \text{ A} \times 10\text{E}3 \text{ }\Omega$) and thus approximately 6.6 V after the tripler. These battery conditions give marginal performance even with the voltage tripling.

[0011] With further use, i.e., further discharge of the lithium battery, the open circuit voltage continues to decrease and the ESR
10 continues to increase to approximately 30 k Ω at EOL. Thus, while a lithium battery in this condition still has considerable charge remaining, the internal impedance and voltage at which the charge is available render a lithium battery unsuitable for continued multi-chamber pacing. Because of this factor, approximately 30-50% of the total typical lithium
15 iodide battery's capacity is not usable and is wasted.

[0012] Thus, the typical lithium/lithium iodine battery currently in use in many implantable cardiac stimulation devices generally requires additional components to deliver the power needed to provide therapy and also has a limited life span in some implementations. Limited
20 life span, of course, requires more periodic follow up and also requires more frequent replacement of the device. As stated above, more frequent replacement of the device is undesirable as it typically requires invasive surgical procedures.

[0013] A further difficulty that occurs with lithium/lithium
25 iodine batteries in implantable medical devices is that the internal configuration of the battery often limits telemetry transfer rates. The power that the lithium battery provides is generally not sufficient to support data transfer rates from implantable cardiac stimulation devices that are in excess of approximately 8 Kbit of data. Typically, the
30 decoupling capacitors are limited to only providing sufficient power to source the pacing pulses but do not have the capacity for providing sufficient charge to maintain voltage during a multi-minute, high speed

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transmission. This relatively low rate of data transfer therefore requires longer download periods to obtain data out of the implantable device which can be very inconvenient for the patient and the treating medical professional as well as consuming additional limited power from the battery.

[0014] Other battery technologies exist that may have application in implantable medical devices, however, these technologies have not generally been used due to implementation problems. One such technology is lithium-carbon monofluoride ($\text{LiCF}_{1.1}$), typically referred to as CF_x batteries. CF_x batteries have some desirable characteristics that show promise for use in implantable medical devices, such as pacemakers. Generally, CF_x batteries have twice the mass energy density as lithium batteries and can thus provide significantly more electrical energy as lithium batteries of similar weight. Moreover, the performance characteristics are comparable to lithium based batteries.

[0015] However, the use of improved battery technologies, such as CF_x batteries, in critical applications, such as implantable cardiac stimulation devices, has been limited by an inability to determine approaching end of life of the battery accurately and efficiently. In applications such as pacemakers and ICDs, it is imperative that the device be replaced prior to the battery failing. Battery failure will result in the device being unable to provide therapeutic stimulation to the heart which can further result in catastrophic consequences for the patient.

[0016] CF_x batteries provide a very stable voltage output throughout their effective life. As a consequence, CF_x batteries do not exhibit output characteristics that are easily modeled to predict the end of life. Basically, these batteries generally maintain a substantially constant output voltage until the end of life and then their power output drops off very precipitously. This makes detection of approaching end of life of the battery extremely difficult. As a consequence, the application of CF_x batteries to patient critical applications, such as pacemakers, ICDs and the like, has been extremely limited.

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Summary of the Invention

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5 **[0021]** In one particular implementation, the voltage monitor includes a band gap reference and a precise A/D, e.g., a 12 bit A/D that compares the battery voltage to the band gap reference voltage. This configuration is capable of monitoring the output voltage of the battery to within 1.0 millivolt resolution. The A/D provides a digital output signal to the microcontroller and the microcontroller evaluates the digital output signal to determine if the battery is approaching end of life. In one aspect, the microcontroller evaluates the signal to first determine a predicted end point of the battery and then evaluates the subsequent signals to determine if the output of the battery is approaching the predicted end point.

10 **[0022]** In one implementation, the microcontroller determines if the battery is approaching end of life by ascertaining a beginning of life voltage of the battery and then determines that end of life is approaching when the battery output voltage has fallen over time back to the beginning of life voltage. In another implementation, the microcontroller determines that end of life is approaching by periodically measuring the output voltage and determining when a peak output voltage has occurred. When measuring voltage, the voltage may vary slightly due to the current being drawn from the battery at any given time. To address this in one implementation, the battery voltage is periodically measured over a pre-selected period of time, e.g., once a day for a week, and a rolling average is maintained. The rolling average can be calculated daily such that the effect of periodic variations in the daily measurements are reduced. The end of life point of the battery is then predicted by determining when the output voltage of the battery has decreased a pre-selected quantity from the peak voltage. In yet another implementation, the end of life point of the battery is approximated by calculating a rate of consumption of the stored battery power and then determining an end of life point at the rate of consumption.

25 **[0023]** Each of the above-implementations can be used in conjunction with others to determine a correlated approaching end of life

point for the battery. Once an end of life point is determined for the implantable medical device, the microcontroller of the implantable medical device can set a register indicating that the end of life has occurred or can otherwise enable an annunciator to advise the patient that it is time to seek replacement of the battery.

[0024] The use of such a monitoring system enables the use of improved batteries, such as CF_x , batteries in patient critical applications. The use of these types of batteries can result in more efficient power consumption, improved data transmission and the like.

These and other objects and advantages of the present invention will be more apparent from the following description taken in conjunction with the accompanying drawings.

Brief Description of the Drawings

[0025] Further features and advantages of the present invention may be more readily understood by reference to the following description taken in conjunction with the accompanying drawings, in which:

[0026] **FIG. 1** is a high-level block circuit diagram of a typical prior art pacemaker employing a lithium battery;

[0027] **FIG. 2** is a graph of typical discharge characteristics of lithium, silver vanadium oxide, and CF_x batteries;

[0028] **FIG. 3** is a simplified diagram illustrating an implantable stimulation device in electrical communication with at least three leads implanted into a patient's heart for delivering multi-chamber stimulation and shock therapy;

[0029] **FIG. 4** is a functional block diagram of a multi-chamber implantable stimulation device illustrating the basic elements of a stimulation device which can provide cardioversion, defibrillation and pacing stimulation in four chambers of the heart;

[0030] **FIG. 5** is a block circuit diagram of a pacemaker provided with a CF_x battery; and

[0031] FIG. 6 is an exemplary flow chart illustrating the battery monitoring process implemented by the device of FIGS. 4 and 5.

Description of the Preferred Embodiments

[0032] The following description is of the best mode presently contemplated for practicing the invention. This description is not to be taken in a limiting sense but is made merely for the purpose of describing the general principles of the invention. The scope of the invention should be ascertained with reference to the issued claims. In the description of the invention that follows, like numerals or reference designators will be used to refer to like parts or elements throughout.

[0033] As shown in FIG. 3, there is a stimulation device 10 in electrical communication with a patient's heart 12 by way of three leads, 20, 24 and 30, suitable for delivering multi-chamber stimulation and shock therapy. To sense atrial cardiac signals and to provide right atrial chamber stimulation therapy, the stimulation device 10 is coupled to an implantable right atrial lead 20 having at least an atrial tip electrode 22, which typically is implanted in the patient's right atrial appendage.

[0034] To sense left atrial and ventricular cardiac signals and to provide left chamber pacing therapy, the stimulation device 10 is coupled to a "coronary sinus" lead 24 designed for placement in the "coronary sinus region" via the coronary sinus os for positioning a distal electrode adjacent to the left ventricle and/or additional electrode(s) adjacent to the left atrium. As used herein, the phrase "coronary sinus region" refers to the vasculature of the left ventricle, including any portion of the coronary sinus, great cardiac vein, left marginal vein, left posterior ventricular vein, middle cardiac vein, and/or small cardiac vein or any other cardiac vein accessible by the coronary sinus.

[0035] Accordingly, an exemplary coronary sinus lead 24 is designed to receive atrial and ventricular cardiac signals and to deliver left ventricular pacing therapy using at least a left ventricular tip electrode 26, left atrial pacing therapy using at least a left atrial ring electrode 27, and

shocking therapy using at least a left atrial coil electrode 28. For a complete description of a coronary sinus lead, see U.S. Patent Application No. 09/457,277, "A Self-Anchoring, Steerable Coronary Sinus Lead" (Pianca et al.), and U.S. Patent No. 5,466,254, "Coronary Sinus Lead with
5 Atrial Sensing Capability" (Helland), which patents are hereby incorporated herein by reference.

[0036] The stimulation device 10 is also shown in electrical communication with the patient's heart 12 by way of an implantable right ventricular lead 30 having, in this embodiment, a right ventricular tip electrode 32, a right ventricular ring electrode 34, a right ventricular (RV) coil electrode 36, and a superior vena cava (SVC) coil electrode 38.
10 Typically, the right ventricular lead 30 is transvenously inserted into the heart 12 so as to place the right ventricular tip electrode 32 in the right ventricular apex so that the RV coil electrode 36 will be positioned in the
15 right ventricle and the SVC coil electrode 38 will be positioned in the superior vena cava. Accordingly, the right ventricular lead 30 is capable of receiving cardiac signals, and delivering stimulation in the form of pacing and shock therapy to the right ventricle.

[0037] As illustrated in FIG. 4, a simplified block diagram is shown of the multi-chamber implantable stimulation device 10, which is capable of treating both fast and slow arrhythmias with stimulation therapy, including cardioversion, defibrillation, and pacing stimulation. While a particular multi-chamber device is shown, this is for illustration
20 purposes only, and one of skill in the art could readily duplicate, eliminate or disable the appropriate circuitry in any desired combination to provide a device capable of treating the appropriate chamber(s) with cardioversion, defibrillation and pacing stimulation.

[0038] The housing 40 for the stimulation device 10, shown schematically in FIG. 4, is often referred to as the "can", "case" or "case electrode" and may be programmably selected to act as the return
30 electrode for all "unipolar" modes. The housing 40 may further be used as a return electrode alone or in combination with one or more of the coil

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electrodes, 28, 36 and 38, for shocking purposes. The housing 40 further includes a connector (not shown) having a plurality of terminals, 42, 44, 46, 48, 52, 54, 56, and 58 (shown schematically and, for convenience, the names of the electrodes to which they are connected are shown next to the terminals). As such, to achieve right atrial sensing and pacing, the connector includes at least a right atrial tip terminal (A_R TIP) 42 adapted for connection to the atrial tip electrode 22.

[0039] To achieve left chamber sensing, pacing and shocking, the connector includes at least a left ventricular tip terminal (V_L TIP) 44, a left atrial ring terminal (A_L RING) 46, and a left atrial shocking terminal (A_L COIL) 48, which are adapted for connection to the left ventricular ring electrode 26, the left atrial tip electrode 27, and the left atrial coil electrode 28, respectively.

[0040] To support right chamber sensing, pacing and shocking, the connector further includes a right ventricular tip terminal (V_R TIP) 52, a right ventricular ring terminal (V_R RING) 54, a right ventricular shocking terminal (R_V COIL) 56, and a superior vena cava (SVC) shocking terminal (SVC COIL) 58, which are adapted for connection to the right ventricular tip electrode 32, right ventricular ring electrode 34, the RV coil electrode 36, and the SVC coil electrode 38, respectively.

[0041] At the core of the stimulation device 10 is a programmable microcontroller 60 which controls the various modes of stimulation therapy. As is well known in the art, the microcontroller 60 typically includes a microprocessor, or equivalent control circuitry, designed specifically for controlling the delivery of stimulation therapy and may further include RAM or ROM memory, logic and timing circuitry, state machine circuitry, and I/O circuitry. Typically, the microcontroller 60 includes the ability to process or monitor input signals (data) as controlled by a program code stored in a designated block of memory. The details of the design and operation of the microcontroller 60 are not critical to the present invention. Rather, any suitable microcontroller 60 may be used that carries out the functions described herein. The use of

microprocessor-based control circuits for performing timing and data analysis functions are well known in the art.

5 [0042] As shown in FIG. 4, an atrial pulse generator 70 and a ventricular pulse generator 72 generate pacing stimulation pulses for delivery by the right atrial lead 20, the right ventricular lead 30, and/or the coronary sinus lead 24 via an electrode configuration switch 74. It is understood that in order to provide stimulation therapy in each of the four chambers of the heart 12, the atrial and ventricular pulse generators, 70 and 72, may include dedicated, independent pulse generators, multiplexed pulse generators, or shared pulse generators. The pulse generators, 70 and 72, are controlled by the microcontroller 60 via appropriate control signals, 76 and 78, respectively, to trigger or inhibit the stimulation pulses.

10 [0043] The microcontroller 60 further includes timing control circuitry 79 which is used to control the timing of such stimulation pulses (e.g., pacing rate, atrio-ventricular (AV) delay, atrial interconduction (A-A) delay, or ventricular interconduction (V-V) delay, etc.) as well as to keep track of the timing of refractory periods, PVARP intervals, noise detection windows, evoked response windows, alert intervals, marker channel timing, etc., which is well known in the art.

15 [0044] The switch 74 includes a plurality of switches for connecting the desired electrodes to the appropriate I/O circuits, thereby providing complete electrode programmability. Accordingly, the switch 74, in response to a control signal 80 from the microcontroller 60, determines the polarity of the stimulation pulses (e.g., unipolar, bipolar, combipolar, etc.) by selectively closing the appropriate combination of switches (not shown) as is known in the art.

20 [0045] Atrial sensing circuits 82 and ventricular sensing circuits 84 may also be selectively coupled to the right atrial lead 20, coronary sinus lead 24, and the right ventricular lead 30, through the switch 74 for detecting the presence of cardiac activity in each of the four chambers of the heart 12. Accordingly, the atrial (ATR. SENSE) and ventricular (VTR.

SENSE) sensing circuits, 82 and 84, may include dedicated sense amplifiers, multiplexed amplifiers, or shared amplifiers. The switch 74 determines the "sensing polarity" of the cardiac signal by selectively closing the appropriate switches, as is also known in the art. In this way, the clinician may program the sensing polarity independent of the stimulation polarity.

[0046] Each sensing circuit, 82 and 84, preferably employs one or more low power, precision amplifiers with programmable gain and/or automatic gain control, bandpass filtering, and a threshold detection circuit, as known in the art, to selectively sense the cardiac signal of interest. The automatic gain control enables the device 10 to deal effectively with the difficult problem of sensing the low amplitude signal characteristics of atrial or ventricular fibrillation. The outputs of the atrial and ventricular sensing circuits, 82 and 84, are connected to the microcontroller 60 which, in turn, are able to trigger or inhibit the atrial and ventricular pulse generators, 70 and 72, respectively, in a demand fashion in response to the absence or presence of cardiac activity in the appropriate chambers of the heart 12.

[0047] For arrhythmia detection, the device 10 utilizes the atrial and ventricular sensing circuits, 82 and 84, to sense cardiac signals to determine whether a rhythm is physiologic or pathologic. As used herein "sensing" is reserved for the noting of an electrical signal, and "detection" is the processing of these sensed signals and noting the presence of an arrhythmia. The timing intervals between sensed events (e.g., P-waves, R-waves, and depolarization signals associated with fibrillation which are sometimes referred to as "F-waves" or "Fib-waves") are then classified by the microcontroller 60 by comparing them to a predefined rate zone limit (i.e., bradycardia, normal, low rate VT, high rate VT, and fibrillation rate zones) and various other characteristics (e.g., sudden onset, stability, physiologic sensors, and morphology, etc.) in order to determine the type of remedial therapy that is needed (e.g., bradycardia pacing, anti-

tachycardia pacing, cardioversion shocks or defibrillation shocks, collectively referred to as "tiered therapy").

[0048] Cardiac signals are also applied to the inputs of an analog-to-digital (A/D) data acquisition system 90. The data acquisition system 90 is configured to acquire intracardiac electrogram signals, convert the raw analog data into a digital signal, and store the digital signals for later processing and/or telemetric transmission to an external device 102. The data acquisition system 90 is coupled to the right atrial lead 20, the coronary sinus lead 24, and the right ventricular lead 30 through the switch 74 to sample cardiac signals across any pair of desired electrodes.

[0049] The microcontroller 60 is further coupled to a memory 94 by a suitable data/address bus 96, wherein the programmable operating parameters used by the microcontroller 60 are stored and modified, as required, in order to customize the operation of the stimulation device 10 to suit the needs of a particular patient. Such operating parameters define, for example, pacing pulse amplitude, pulse duration, electrode polarity, rate, sensitivity, automatic features, arrhythmia detection criteria, and the amplitude, waveshape and vector of each shocking pulse to be delivered to the patient's heart 12 within each respective tier of therapy.

[0050] Advantageously, the operating parameters of the implantable device 10 may be non-invasively programmed into the memory 94 through a high-speed telemetry circuit 100 in telemetric communication with the external device 102, such as a programmer, transtelephonic transceiver, or a diagnostic system analyzer. The high-speed telemetry circuit 100 is activated by the microcontroller by a control signal 106. The high-speed telemetry circuit 100 advantageously allows intracardiac electrograms and status information relating to the operation of the device 10 (as contained in the microcontroller 60 or memory 94) to be sent to the external device 102 through an established communication link 104.

[0051] For examples of such devices, see U.S. Patent No. 4,809,697, entitled "Interactive Programming and Diagnostic System for use with Implantable Pacemaker" (Causey, III et al.); U.S. Patent No. 4,944,299, entitled "High Speed Digital Telemetry System for Implantable Device" (Silvian); and U.S. Patent Application No. 09/223,422, filed 12/30/1998, entitled "Efficient Generation of Sensing Signals in an Implantable Medical Device such as a Pacemaker or ICD" (note: this relates to transfer of EGM data) (McClure et al.), which patents are hereby incorporated herein by reference.

[0052] In the preferred embodiment, the stimulation device 10 further includes a physiologic sensor 108, commonly referred to as a "rate-responsive" sensor because it is typically used to adjust pacing stimulation rate according to the exercise state of the patient. However, the physiological sensor 108 may further be used to detect changes in cardiac output, changes in the physiological condition of the heart 12, or diurnal changes in activity (e.g., detecting sleep and wake states). Accordingly, the microcontroller 60 responds by adjusting the various pacing parameters (such as rate, AV Delay, V-V Delay, etc.) at which the atrial and ventricular pulse generators, 70 and 72, generate stimulation pulses.

[0053] The stimulation device 10 additionally includes a battery 110 which acts as a primary power source to provide operating power to all of the circuits shown in FIG. 4. For the stimulation device 10, which employs shocking therapy, the battery 110 must be capable of operating at low current drains for long periods of time (preferably less than 10 μ A) and then be capable of providing higher current pulses (for capacitor charging) when the patient requires a shock pulse (preferably, in excess of 100 mA, at voltages above 2 V, for periods of 100 seconds or more). The battery 110 must also have a predictable discharge characteristic so that elective replacement time can be detected. In this embodiment, the battery 110 is a carbon monofluoride (CF_x) battery. An example of a

suitable CF_x battery 110 is the model 9424 available from Wilson Greatbatch Ltd. of Clarence, New York.

[0054] The battery 110 preferably has an output voltage 120 that varies as the battery 110 delivers electrical charge 122 as illustrated in FIG. 2. The voltage 120 of the battery 110 has a beginning voltage 124, a peak voltage 126, and an end-of-life voltage 130. In this embodiment, the battery 110 has a beginning of life voltage that is approximately 2.4 volts and the output voltage 120 then rises to a peak voltage of approximately 2.72 volts after delivering approximately 600 milliampere-hours of charge 122. Subsequently, the output voltage decreases to a transition point, corresponding, in this implementation, to the end of life point 130, where the output voltage corresponds to the beginning of life voltage 124. It should be appreciated that the beginning voltage 124 and peak voltage 126 of the battery 110 of this embodiment are of different values and occur at different points of charge 122 delivery. Moreover, for any given battery, the magnitude of the beginning of life voltage, peak voltage, etc. can vary. However, for CF_x batteries, the output voltage typically demonstrates the curve shown in FIG. 2.

[0055] In the implementation illustrated in FIG. 2, the CF_x battery provides a particularly stable normal output voltage over a significant period of the battery's life, e.g., a variation of less than 150 millivolts DC over 1700 milliampere hours. As is generally illustrated, this particular battery provides an output voltage that is approximately 2.7 volts DC. The end of life point 130 is selected for the battery at a point where the battery 110 is transitioning between normal output and end of life where the output voltage begins to drop off significantly. As is illustrated in FIG. 2, the battery drops more than 1,900 millivolts in less than 400 milliampere hours. The relatively stable output voltage followed by the transition to a relatively sudden decrease in output voltage requires that additional monitoring be employed to ensure that the end of life point is detected.

[0056] The battery 110 of this embodiment is generally semicircular and approximately 45 mm x 22 mm x 5 mm. The battery 110 of this embodiment weighs approximately 7.6 g which is almost 7 g, or 47%, less than a comparable lithium-iodide battery. The battery 110 of this embodiment also has a beginning-of-life (BOL) equivalent series resistance (ESR) 132 of approximately 10 Ω and the battery's 110 ESR 132 increases to only approximately 1 k Ω near the battery's 110 end-of-life (EOL) 130. Thus, the battery 110 of this embodiment offers a substantially lower ESR 132 than the BOL ESR of approximately 300 Ω and EOL ESR of approximately 30 k Ω for a comparable lithium battery.

[0057] The lower ESR 132 of the battery 110 of this embodiment throughout its useful life provides an extended longevity of the device 10. In particular, the lower internal resistance of the battery 110 results in significantly less of the power of the battery 110 being dissipated as heat energy in the battery 110. Thus more battery power is available to be supplied to the component devices of the implantable medical device.

[0058] Specifically, with a standard lithium iodine battery as discussed above, the equivalent series resistance is typically so high that a de-coupling capacitor must be continuously charged in order to provide the needed power during pacing pulses. Since the equivalent series resistance of the CF_x battery 110 is so much lower than the equivalent series resistance of the standard lithium iodine battery, the need for a de-coupling capacitor is reduced which thereby frees up more space for other components and can allow for more compact implantable devices.

[0059] Moreover, the voltage and internal resistance characteristics of the battery 110 of this embodiment enable effective pacing with a voltage doubler 142 as illustrated in FIG. 5 as opposed to the voltage tripler of prior art pacers. The voltage doubler 142 doubles the voltage of signals provided to the voltage doubler 142 and is fabricated in a known manner. As discussed above, the lowered internal resistance of

the battery 110 results in significantly less energy being dissipated as heat and thus allows for more current to be supplied to the leads 20, 24, 30. The use of a voltage doubler as opposed to a voltage tripler results in less dissipation of energy thereby prolonging battery life.

5 **[0060]** As discussed above in connection with **FIG. 2**, the end of life characteristics of the battery 110 are less easily predicted than prior art battery types. Specifically, a lithium iodine battery and an SVO type battery have a generally gradual decline in the output voltage versus milliamperere hours. The gradual decline thus allows for greater
10 predictability for when the battery is reaching end of life necessitating removal and replacement. However, as is illustrated in **FIG. 2**, the CF_x battery 110 provides a substantially constant output voltage over a significant portion of its life and then it experiences a relatively sharp decline in the output voltage and the end of life. It is for this reason that
15 CF_x batteries have not been used in many implantable medical devices, the sudden end of life of the battery inhibits prior detecting of its imminent approach. To address this particular problem, the implantable medical device 10 incorporates a precision detector to detect voltages to predict and determine the approaching end of life of the battery to thereby allow
20 the use of CF_x batteries in more critical applications, such as implantable cardiac stimulation devices.

[0061] **FIG. 5** is a schematic block diagram which illustrates the basic configuration of the battery monitoring circuit 134, the
 microcontroller 60, the telemetry 100 and the various components
25 allowing for delivery of pacing pulses to the leads. As is indicated in **FIG. 5**, the CF_x battery 110 is used to power a known band gap reference 136. As is understood, the typical band gap reference provides a very stable reference output that is both temperature and input voltage independent. Hence, variations in the output voltage developed by the battery 110 does
30 not affect the output of the band gap reference 136 provided that the battery output is greater than a preselected minimum. In one implementation, the band gap reference 136 will provide an output voltage

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[0064] As is be discussed in greater detail below, the end-of-life
30 point can be determined using several different techniques without
departing from the spirit of the present invention. In one particular
implementation, the CF_x battery 110 exhibits the output voltage

characteristic as illustrated in **FIG. 2**. At initiation, or beginning of life, the battery 110 exhibits an initial beginning of life output voltage. The battery 110 then, over time, provides a somewhat increased output voltage until such time that the battery 110 is beginning to approach end of life.

- 5 Consequently, in this implementation, the microcontroller 60 determines a predicted end of life as being the time period at which the voltage measured by the monitoring circuit 134 is equal to the originally measured beginning of life voltage.

- [0065]** In another implementation, the microcontroller 60
10 periodically receives the signal from the precise A/D converter 140 indicative of the battery 110 voltage and it then determines when a peak voltage has occurred. The microcontroller 60 then determines that the predicted end of life period occurs when the signal from the monitoring circuit 134 indicates that the output voltage has decreased a preselected
15 amount from the peak voltage.

- [0066]** In yet another implementation, the microcontroller 60 includes a software routine that implements a fuel gauge. The output power provided by the battery 110 is either measured or modeled and, after a predetermined amount of power has been consumed, the
20 microcontroller 60 then determines end of life has occurred. Naturally, any of the three preceding implementations can be correlated and used together to determine the end of life of the battery 110 without departing from the spirit of the invention.

- [0067]** In implementations where the voltage is measured,
25 the measurement circuit 134 periodically provides a signal to the microcontroller 60, e.g., on a daily basis. The microcontroller 60 preferably processes the signal so as to determine a value indicative of the output voltage for the battery 110. The actual battery voltage measured on any given day may vary slightly due to events occurring at
30 about the time the measurement is taken. For example, successive rapid discharges of the battery 110 immediately prior to taking the measurement may result in a temporarily lower reading.

[0068] The microcontroller 60 is preferably configured to take each of the individual measurements provided by the measurement circuit 134 and normalize it with respect to previous and/or subsequently obtained measurements. In one implementation, the microcontroller 60
5 uses a rolling average algorithm where each measurement is averaged together with a pre-selected number of previous and/or subsequent measurements to define a measurement value for a particular time period. Any number of different normalization techniques can be used to accommodate temporary variations in the measured output voltage of the
10 battery 110 without departing from the spirit of the invention.

[0069] FIG. 6 is a flow chart which illustrates one possible operation of the microcontroller 60 as it implements a first algorithm for determining the end of life point of the battery 110. This particular algorithm is simply exemplary and it can use either the beginning of life
15 voltage or the peak voltage to determine a predictive end of life point of the battery 110. In particular, from a start state 200, the microcontroller 60 records the beginning of life voltage 202 upon power up of the implantable medical device 10.

[0070] Subsequently, the microcontroller 60 continues to
20 periodically sample, in state 204, the output voltage provided by the A/D converter 240. The microcontroller 60 is preferably programmed so as to sample a signal indicative of the measured voltage at a periodic rate, e.g., every 12 hours or every 24 hours. The microcontroller 60 then preferably normalizes the sampled measurement in state 205. As discussed above,
25 normalizing the sampled measurement reduces the impact of temporary variations in the battery 110 in the determination of end of life of the battery 110 and any of a number of normalization techniques can be used to obtain a normalized value including using the rolling averaging technique described above.

[0071] As is indicated in FIG. 6, the microcontroller 60 will
30 also determine, in decision state 206, whether a peak voltage has been measured in state 204. The manner in which the microcontroller 60

determines whether a peak voltage has been determined can be done in any of a number of ways including evaluating a plurality of previously received normalized voltages for a peak followed by a subsequent decline in the normalized voltages. In one implementation, the end of life of the battery 110 is based upon a detected preselected decrease from the peak normalized voltage.

[0072] In another implementation, the time at which the peak normalized voltage has occurred can also be recorded by the microcontroller 60 such that this time component can provide a half-life indication of the remaining battery life on the assumption that the peak will occur at approximately half the usage of the battery 110. If the peak normalized voltage has been determined, the microcontroller 60 then records, in state 210, the peak normalized voltage and a time indication as to when it occurred. The microprocessor 60 then determines the predictive end of life point 130 of the battery 110. As described above in connection with **FIG. 2**, the predicted end of life point 130 preferably corresponds to the transition between the relatively constant normal output voltage of the battery 110 and the region of the battery curve where the battery 110 output voltage begins to decline precipitously.

[0073] The predicted end of life can either correspond to the time period at which the normalized sampled output voltage corresponds to the beginning of life voltage measured in state 202 or it can correspond to the time at which the normalized sampled output voltage has a magnitude that is a pre-selected amount less than the peak voltage. Hence, in this implementation, the microprocessor 60 is determining a voltage value that will occur in the future that is indicative of the battery 110 approaching an end of life condition. The microcontroller 60 continues to receive samples from the battery monitoring circuit 134 and it then can compare these values to the voltage value corresponding to the predicted end of life point of the battery 110.

[0074] Hence, the microcontroller 60 then determines, in decision state 212, whether the last measured normalized output voltage

in state 204 is indicative of the end of life of the battery 110 in decision state 212. If it is not, the microcontroller 60 continues to sample the output voltage in state 204 as previously described until the end of life voltage is detected. As discussed above, several different

5 implementations can be used to determine end of life of the battery 110.

[0075] Once the end of life point has been determined by the microcontroller 60 to have been met, the microcontroller 60 provides an indication of the approaching end of life such that upon subsequent monitoring of the implantable medical device 10 via the telemetry circuit 100, a treating medical professional will be advised by the microcontroller 60 that the end of life point of the battery 110 has been detected thereby allowing the treating medical professional to take appropriate action to replace the battery 110 or the implantable device 10. The microcontroller 60 can also be configured to initiate an annunciator to advise the patient that the end of life of the battery 110 is approaching to thereby induce the patient to seek assistance from a medical professional. These annunciators can either take the form of an audible tone or an electrical discharge that is sensed by the patient.

[0076] It will be further appreciated that, in some circumstance, it may be desirable to include other end of life detecting technologies in order to ensure that end of life of the battery 110 is accurately predicted and detected. For example, the use of a fuel gauge, which either measures or models the amount of power dissipated over time, can also be used by the implantable medical device 10 to determine approaching end of life. The fuel gauge routine can be a software implementation used by the microcontroller 60 to model or detect energy output so as to both provide a prediction as to when the end of life transition point will occur and whether the energy loss from the battery 110 is indicating that the end of life is approaching. This technique can be used in conjunction with the techniques described above in connection with FIG. 6 to improve the accuracy of both predicting when end of life will occur for a particular battery 110 and whether it has occurred.

[0077] From the foregoing it will be appreciated that the use of a sophisticated battery monitoring technique allows for the use of improved batteries such as a CF_x battery 110 that has a precipitous end of life decline in battery voltage. The use of this type of battery 110 reduces the
5 need for a decoupling capacitor in a pacemaker application and also allows for higher speed telemetry operation. Moreover, the significantly reduced internal resistance of the battery 110 further results in significantly less energy being consumed internally by the battery 110 which thereby results in more efficient use of the battery power and also
10 does not require the use of voltage triplers in pacing applications.

[0078] Although the foregoing description of the preferred embodiment of the present invention has shown, described, and pointed out the fundamental novel features of the invention, it will be understood that various omissions, substitutions, and changes in the form of the detail
15 of the apparatus as illustrated, as well as the uses thereof, may be made by those skilled in the art without departing from the spirit of the present invention. Consequently, the scope of the present invention should not be limited to the foregoing discussions, but should be defined by the appended claims.

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